

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	10	read\$3 with phase adj change with threshold adj voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/22 10:12
L3	2	L2 and wireless	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/22 10:21
L4	62	processor and wireless adj interface adj coupled with processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/22 10:14
L5	33	4 and phase adj change adj memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/22 10:15
L6	6	5 and read and threshold adj voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/22 10:21
S1	2	"6141241".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/21 17:19
S2	10	read\$3 with phase adj change with threshold adj voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/22 10:12